



VIDYA PRATISHTHAN'S KAMALNAYAN BAJAJ INSTITUTE OF ENGINEERING & TECHNOLOGY, BARAMATI

Department of Electronics & Telecommunication Engineering

Departmental Master Time Table 2025-26

SEM-I

(w.e.f - 1st July 2025)

Day	Time	9.15 To 10.15	10.15 To 11.15	11.15 To 11.30	11.30 To 12.30	12.30 To 01.30	1.30 To 2.45	02.45 To 03.45	03.45 To 04.45	04.45 To 05.45
	Class/ Period	1	2		3	4		5	6	7
Monday	S.Y.B.Tech	DLD-A (DE Lab) BHP S&S-B (SSD Lab) PNA AC-D (COMM LAB) SRS			DLD - BHP (14)	S&S-PNA (14)		S&S-D (SSD Lab) PNA VSC-A (SP Lab) - RSP AC-C (COMM LAB) SRS		Community Project
	T.Y.B.Tech	AWP-(MMJ) (14)	ES&RTOS-(VSS) (14)		ES&RTOS-A (SP Lab) VSS DSP-B (VLSI LAB) NSB AWP-D (COMM Lab) MMJ			HON-RKS(15)	UHV-Conf Hall	OE- (15)
	B. E. (A)	EL2-DL- NSB (15)	CC-RSP (15)		EL1 IoT-SDB / PLC (15)			EL-I-B (VLSI Lab) SDB /MP RMT-A (RMT Lab) MMJ		
Tuesday	S.Y.B.Tech	DLD-B (DE Lab) BHP AC-A (COMM LAB) SRS			DLD - BHP (14)	AEM-SSK (14)		AC - SRS (14)	S&S-PNA (14)	HON-RKS(14)
	T.Y.B.Tech	HON-RKS(14)	DSP-PNA (14)		AWP-A (COMM Lab) MMJ ES&RTOS-B (SP Lab) VSS DSP-C (VLSI LAB) PNA			ES&RTOS-(VSS) (15)	AWP-(MMJ) (15)	Community Project
	B. E.(A)		CC-RSP (15)		EL1 IoT-SDB / PLC (15)	EL2-DL- NSB (15)		EL-I-C (VLSI Lab) SDB /MP CC- A (SP LAB) RSP		
Wednesday	S.Y.B.Tech	DLD-C (DE Lab) BHP S&S-A (SSD Lab) PNA VSC-D (SP Lab) - SRS			DLD - BHP (14)	S&S-PNA (14)		AEM-SSK (14)	HON-RKS(14)	MDM- (14)
	T.Y.B.Tech	UHV-Conf Hall	ES&RTOS-(VSS) (14)		ES&RTOS-C (SP Lab) VSS AWP -B (COMM Lab) MMJ DSP-D (VLSI LAB) NSB			DSP-PNA (15)	AWP-(MMJ) (15)	MDM-(15)
	B. E.(A)	RMT-MMJ (15)	CC-RSP (15)		EL1 IoT-SDB / PLC (15)			CC- B (SP LAB) -RSP EL-I-A (VLSI Lab) SDB /MP RMT-C (RMT Lab) BHP		
Thursday	S.Y.B.Tech	DLD-D (DE Lab) BHP VSC-C (SP Lab) - RSP AC-B (COMM LAB) SRS			AEM-SSK (14)	AC - SRS (14)	Lunch Break	Hon VLSI- (VLSI Lab) RKS		
	T.Y.B.Tech	AC-Conf Hall	DSP-PNA (14)		ES&RTOS-D (SP Lab) VSS AWP-C-(COMM LAB) MMJ DSP-A (VLSI LAB) PNA			MDM-A (SP LAB)		OE- (15)
	B. E.(A)	RMT-MMJ (15)	EL2-DL- NSB (15)					CC- C (VLSI LAB) -RSP RMT-B (RMT Lab) MMJ		
Friday	S.Y.B.Tech	AC - SRS (14)	MDM- (14)		MDM-A (SP LAB) VSC-B (COMM LAB) SRS Community Project S&S-C (SSD Lab) PNA			MDM-B (SP LAB) Community Project		
	T.Y.B.Tech		MDM- (15)		MDM- B (VLSI LAB)			Hon VLSI- (VLSI Lab) RKS Community Project		HON-RKS (14)
	B. E.(A)	VLSI-SB(204)	VLSI-SB(204)		RMT-MMJ (15)			VLSI (VLSI Lab)-SB		
Saturday	S.Y.B.Tech	MDM-C (SP LAB)			MDM-D (SP LAB)					
	T.Y.B.Tech	MDM-C (VLSI LAB)			MDM-D (VLSI LAB)					
	B. E.(A)	VLSI-SB(15)			VLSI (VLSI Lab)-SB			VLSI (VLSI Lab)-SB		

SHORT RECESS

Lunch Break

Time-Table In-Charge
R.S.Piske

Time-Table Co-ordinator
M.D. Shelar

H.O.D.
Dr B.H.Patil

Principal
Dr. P. S. Patil

S.Y.B.Tech

AEM :-Advance Engineering Mathematics- S.S.Khot

AC:-Analog Circuits :- S.R.Sabale

DLD:- Digital Logic Design :- Dr. B.H. Patil

S&S:Signal &System :- Dr. Arotale P.N.

VSC: Programming in Python:- S.R.Sabale/R.S.Piske

MDM DS :-Multidisciplinary Minor Course-Data Structures:- Devika Mehta

HON- Honor -Dr. R.K.Shastrri

CP:Community Project:-PNA/VSS/NSB/MMJ

T.Y.B.Tech

ES&RTOS:-Embedded System & RTOS :- V.S.Surwase

DSP-Digital Signal Processing :-N.S.Biradar/Dr.P.N.Arotale

PEL-I(AWP)Antennas andWave Propagation:Dr.M.M.Jadhav

MDM :-Multidisciplinary Minor Course:-

OE: Open Elective

AC: Audit Course

HON- Honor System Verilog For Design AndVerification -Dr.R.K.Shastrri

CP:Community Project:-RKS/SDB/RSP/SRS

BE A

RMT: Radiation & Microwave Theory: Dr. M.M.Jadhav

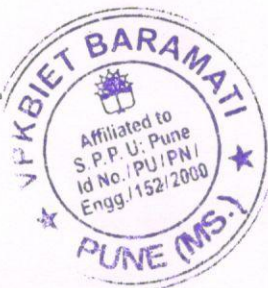
VLSI : VLSI Design :Shrikant Bhisade

CC : Cloud computing: R.S.Piske

EL-I:PLC SCADA and Automation: Mayur Phadtare

EL-I: IOT :- S.D. Biradar

EL-II :- (DL)Deep Learning :-N.S.Biradar



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